

SECRET

Class	Substance

ISSUE 0

PATENT NUMBER

U.S. UTILITY Patent Application

Q.I.P.E.	PATENT DATE
SCANNED TR3 01 Am	

APPLICATION NO. 09/683486	CONT/PRIOR	CLASS 257	SUBCLASS 301	ART UNIT 2811	EXAMINER C. G. Brennan
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Andres Bryant
K. Muller
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Fin-based double poly dynamic threshold CMOS FET with spacer gate and method of fabrication

PTO-2040
12/89

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ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg	Figs Drwg	Print Fig	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____.				ISSUE FEE	
	(Primary Examiner)			Amount Due	Date Paid
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	(Legal Instruments Examiner)				

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